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CONFIRMATION NO. 4403

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APPLICANTS Hiroyuki Takahashi, Kanagawa, JAPAN; Takuya Hirota, Kanagawa, JAPAN; Noriaki Komatsu, Kanagawa, JAPAN; Atsushi Nakagawa, Kanagawa, JAPAN; Susumu Takano, Kanagawa, JAPAN; Masahiro Yoshida, Kanagawa, JAPAN; Yuuji Torige, Kanagawa, JAPAN; Hideo Inaba, Kanagawa, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP02/13555 12/25/2002 <i>yes, TL</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2001-397575 12/27/2001 <i>yes, TL</i>				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met <i>TL</i> Allowance Verified and Acknowledged Examiner's Signature _____ Initials _____		STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 18
		INDEPENDENT CLAIMS 4		
ADDRESS Choate Hall & Stewart Exchange Place 53 State Street Boston, MA 02109-2804				
TITLE Semiconductor storage device and refresh control method thereof				
FILING FEE RECEIVED 920	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	